



Application of Copper Atomic Layer Deposition to Solar Cells



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Market

Energy harvesting: Solar Cells

- Lately, the solar sector has rebounded strongly and displayed potential for further growth
- Solar prices continue to approach grid parity
- Increasing efficiency abets decreasing costs



Fig. (1): Array of solar cells

Additional markets:

- Communications
 - Increase data transfer capacity
- Microelectronics
 - Nanoscale integrated circuits

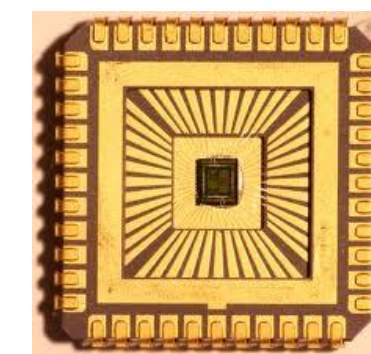


Fig (2): Microchip used in micro-electronics

Technology

Solar Collection: Rectenna

- Optical rectification diode
- Serves two purposes: collects solar radiation and converts to useable DC current by way of electron tunneling
- Effectiveness driven by pointed geometry of the rectenna device

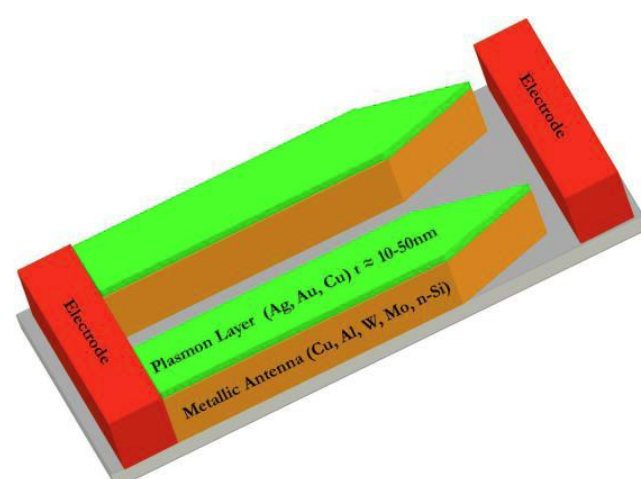
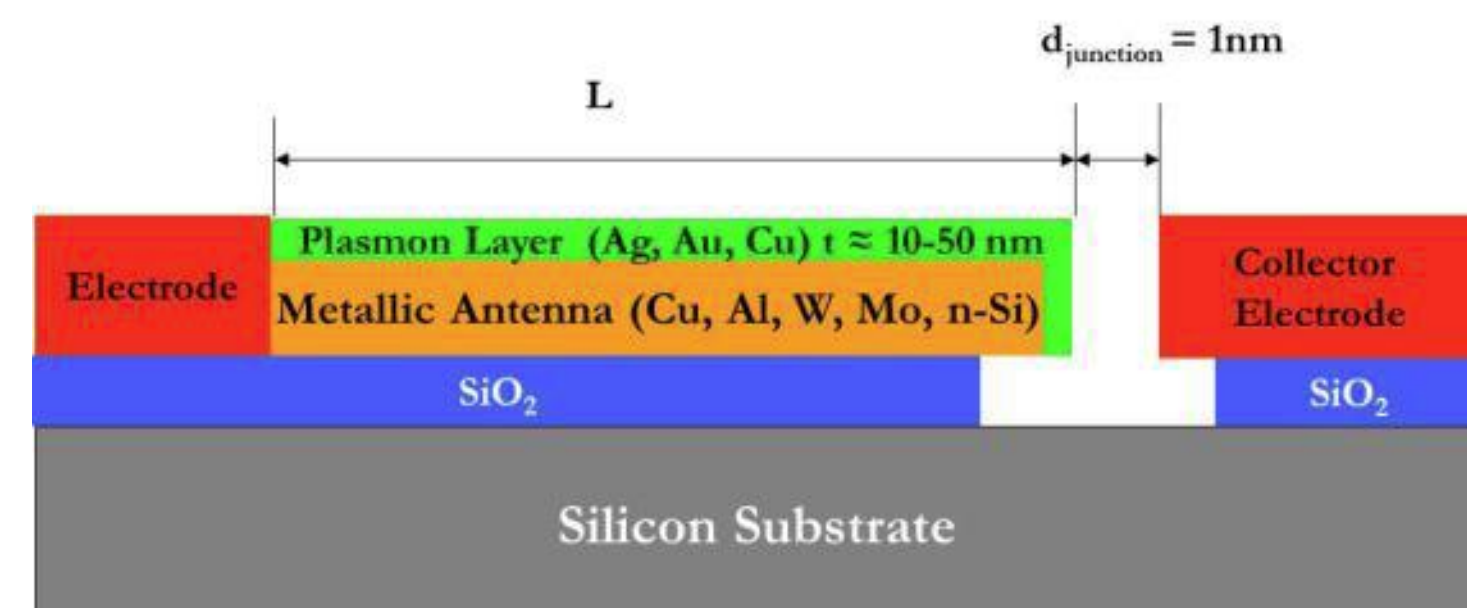


Fig. (3): Overhead view of rectenna and electrodes



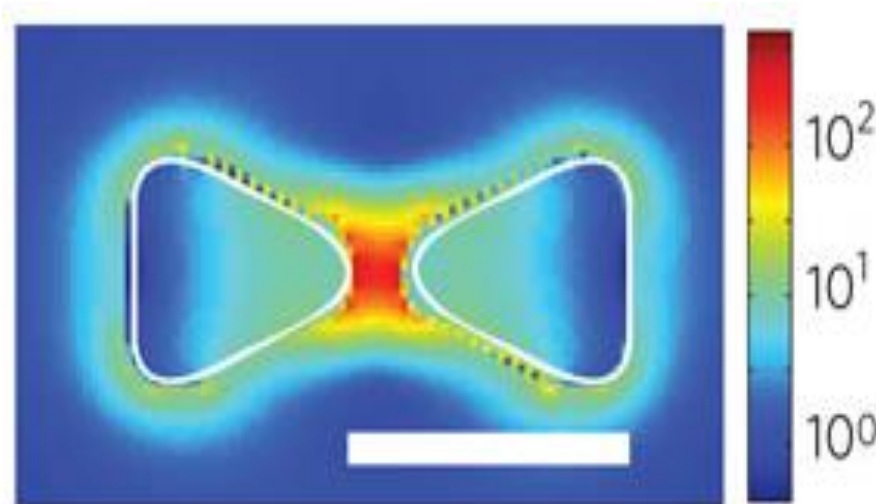
Manufacture: Atomic Layer Deposition (ALD)

- Highly selective growth of atomic layers of metals on substrates
- More controlled version of chemical vapor deposition (CVD)

This work endeavors to optimize the copper ALD process for the manufacture of ideal and reproducible rectenna devices for solar cells.

Concept

- Plasmonic enhancement occurs at sharp edges
- Enhanced field at tip leads to better electron transport and increased efficiency



Methods

Atomic Layer Deposition Experimental Setup

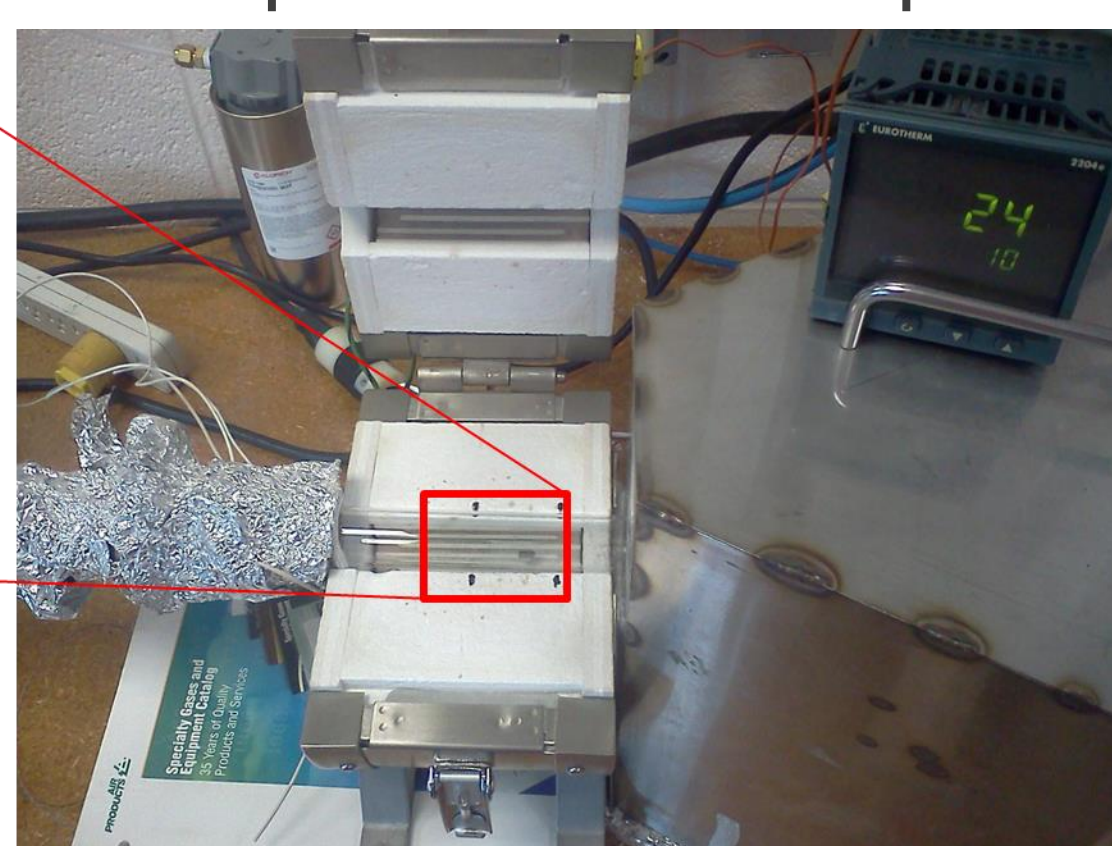
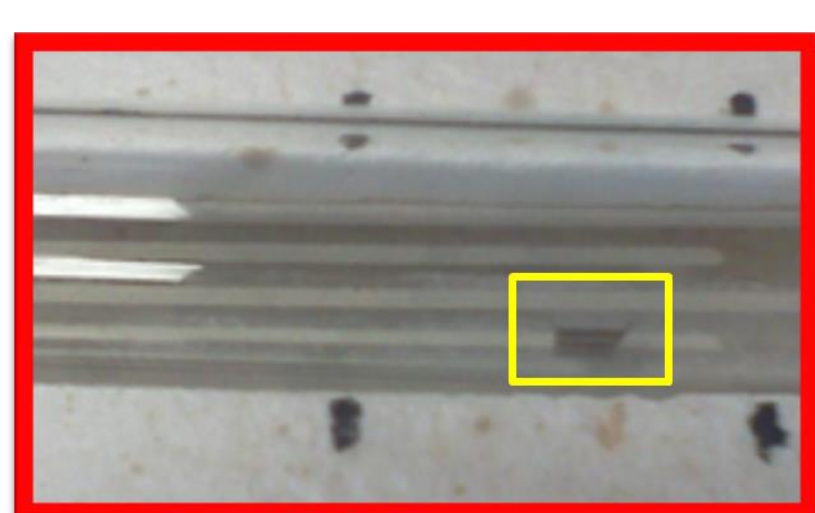


Fig. (5): Experimental setup with palladium coated sample highlighted

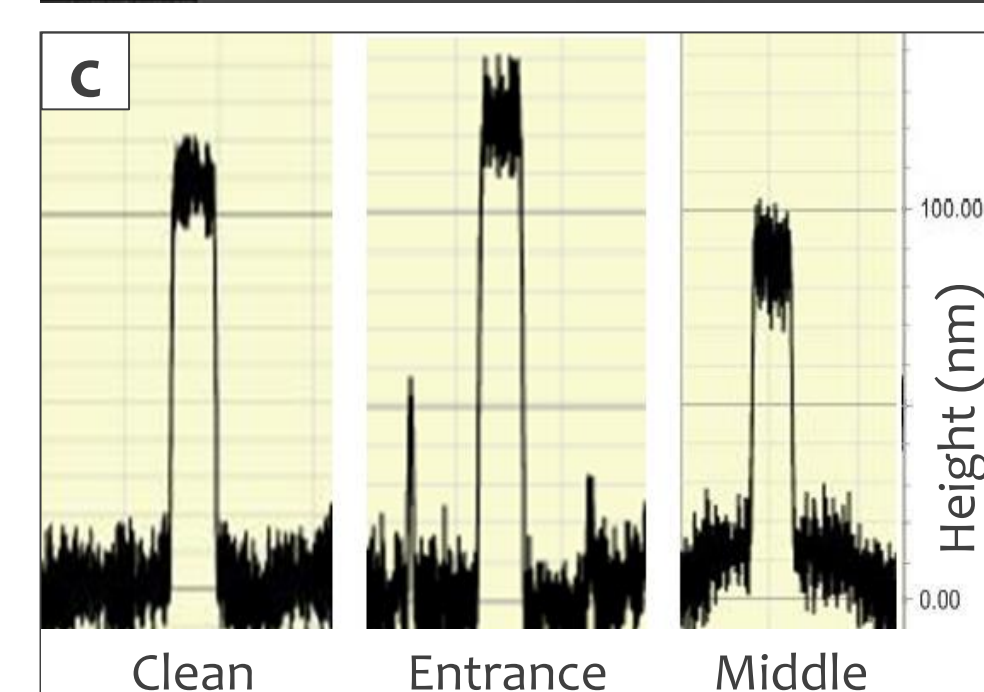
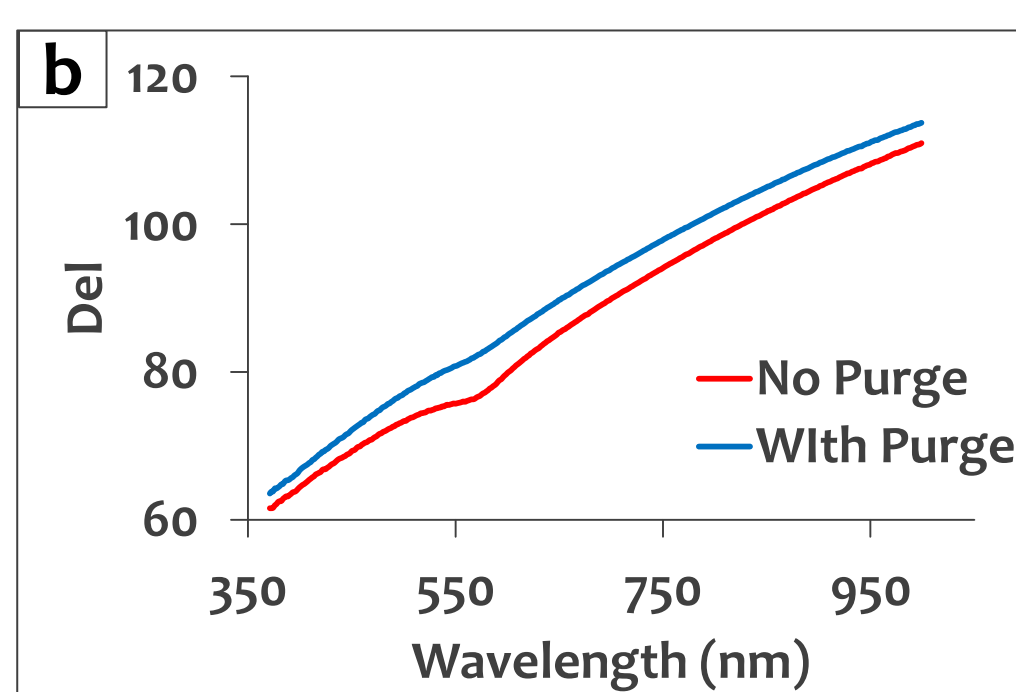
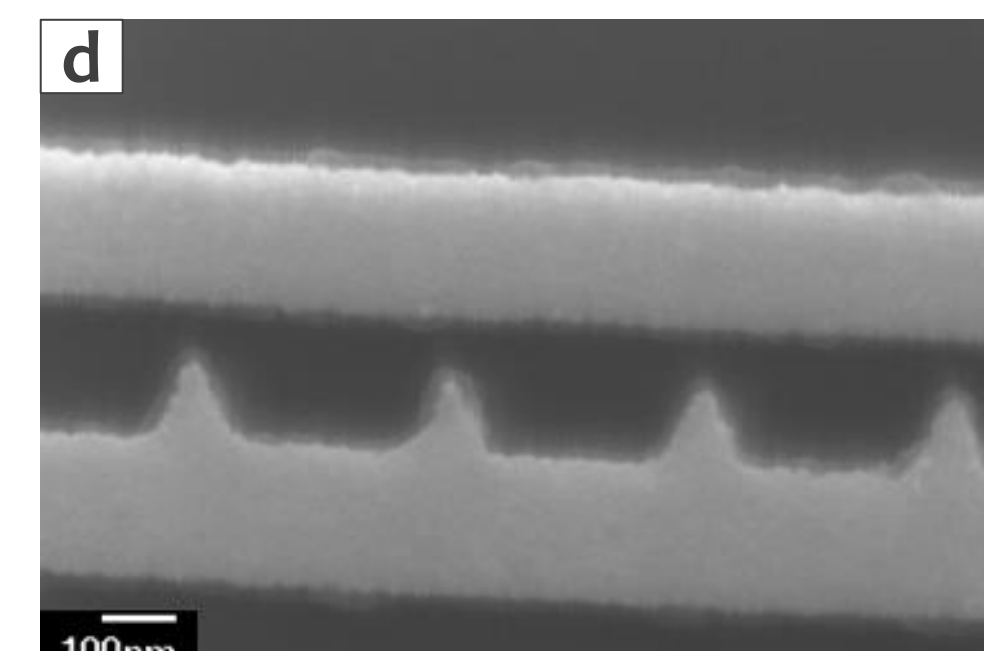
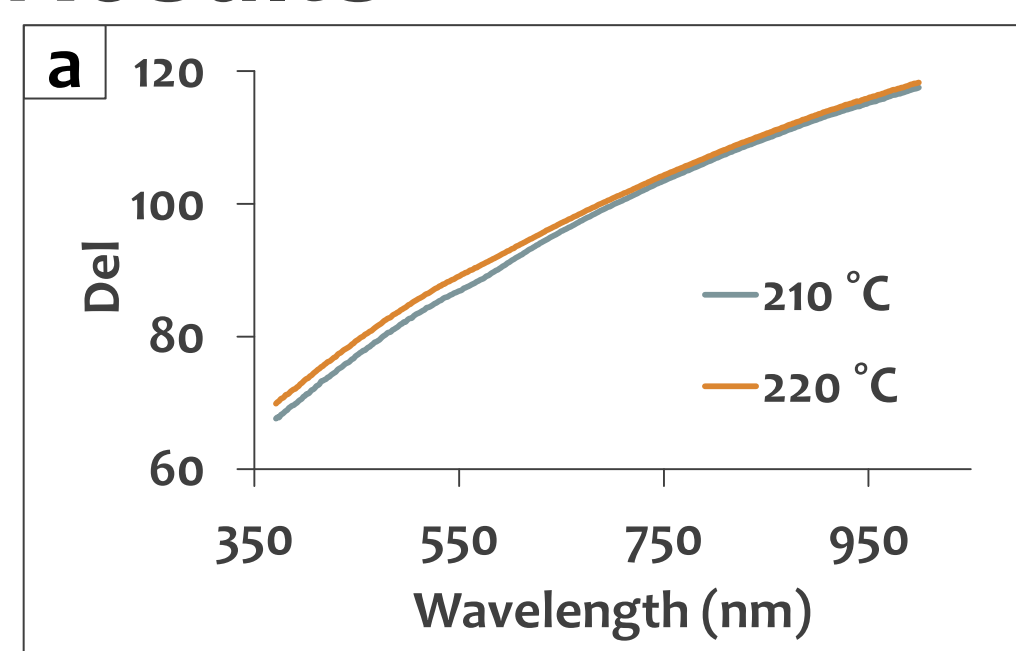
Variable Conditions

- Temperature
- Time length of 2nd purge
- Measurement techniques
 - SEM imaging (growth, uniformity analysis)
 - Ellipsometry (purity of deposited layer analysis)
 - DEKTAK profiling (growth, uniformity analysis)

Conclusions

- CVD plays a role when no 2nd purge stage is used
- Occurs due to mixing of H₂ and Cu in reactor
- The effects of short times (1-2 seconds) for 2nd purge should be examined in future experiments
- Temperature of reactor is highly important
 - Layer desorption significant at higher temperatures
 - Loss of selectivity occurs at higher temperatures

Results



- Pure palladium samples show better growth at 210°C than 220°C over the same number of cycles.
- The copper growth is much greater when no 2nd purge is used. The dip in the Del line at ~550nm indicates that the layer is pure copper.
- Surface profiles of device samples showing the different step heights of features of a clean sample, and samples placed at the entrance and near the middle.
- Scanning electron microscope image of sample substrates mimicking the rectenna device before copper is applied by ALD

Location	Height	Growth	Growth/Cycle	Copper on Pd	Copper on SiO ₂
Entrance	118.3 nm	18.3 nm	.037 nm/cycle	23%	0%
Exit	109.5 nm	9.5 nm	.019 nm/cycle	20%	0%

Table 1: Samples closer to entrance show greater copper growth, suggesting that CVD could play a role in the process. The growth rate per cycle is consistent with what is expected of a copper ALD reaction however.

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- UConn: Professors: Brian Willis, Rich Dino, Jeff McCutcheon
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Citations:
 Fig 1: <http://solarenergypanelshop.com/wp-content/uploads/Monocrystalline-Solar-Panel.jpg>
 Fig 2: <http://www.sjalander.com/research/images/chip2006.jpg>
 Fig 3,4: Scitech Solar
 Fig 5: Kinkhabwala, A., Yu, Z., Fan, S., Avlasevich, Y., Müllen, K., Moerner, W.E.
 Large single-molecule fluorescence enhancements produced by a bowtie nanoantenna. (2009) Nature Photonics, 3 (11), pp. 654-657.

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